

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

EDWARD GROCHOWSKI, ET AL.

Application No.: Unknown

Filed: March 31, 2004

For: **A Method And System To Provide
User-Level Multithreading**

Art Group: Unknown

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: March 31, 2004

Sanjeet K. Dutta
Sanjeet K. Dutta, Reg. No. 46,145

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (408) 947-8200

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Geneva Walls
Geneva Walls

3/31/04

Date

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Sheet

1

of

2

Complete if Known

Application Number	Unknown
Filing Date	March 31, 2004
First Named Inventor	Edward Grochowski
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	42P18305

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
		ALPERT, DON, "Will Microprocessors Become Simpler?", Multithreading May Reverse the Trend Toward Increasing Processor Complexity, Microprocessor Report, The Insider's Guide To Microprocessor Hardware, www.MPRonline.com, November 17, 2003, pp. 1-4.	
		GONTMAKHER, ALEX, et al., "Intrathreads: Techniques For Parallelizing Sequential Code", Abstract, gsash@cs.technion.ac.il, pp. 1-9.	
		INTEL, "Introduction To The IA-32 Intel Architecture, Chpt. 2, pp. 1-13.	
		KISSEL, KEVIN D., "Multithreading Extensions To The MIPS® Architecture", MIPS Technologies, At the core of the user experience.™, Copyright © 2003 MIPS Technologies, Inc. All rights reserved.pp. 1-23	
		SUN MICROSYSTEMS, "MAJCTM Architecture Tutorial", Palo Alto, CA 94303, USA, Internet: www.son.com/microelectronics, pp. 31	
		LEVY, MARKUS, "Multithread Technologies Disclosed At MPF", Imagination and MIPS Discuss Details of New Architectures, Microprocessor Report, The Insider's Guide To Microprocessor Hardware, www.MPRonline.com, November 10, 2003, pp. 1-4.	
		SIGNORELLI, ROBERT D., "MAJC: Innovation In Parallelism", pp. 1-6.	

Examiner Signature	Date Considered
--------------------	-----------------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Sheet

2

of

2

Application Number

Unknown

Filing Date

March 31, 2004

First Named Inventor

Edward Grochowski

Art Unit

Unknown

Examiner Name

Unknown

Attorney Docket Number

42P18305

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SOHI, GURINDAR S., et al., "Speculative Multithreaded Processors", Cover Feature, University of Wisconsin-Madison, pp. 66-72.	
		SRIRAM, PARTHASARATHY, et al., "MPEG-2 Video Decompression On A Multi-Processing VLIW Microprocessor", Sun Microsystems, Inc., 901 San Antonio Road, Palo Alto, CA 94303, USA, Presented at ICCE, June 2000, Los Angeles, CA, USA, © IEEE 2000, pp. 2.	
		SUDHARSANAN, SUDHARSANAN, "MAJC-5200: A High Performance Microprocessor For Multimedia Computing", Sun Microsystems, Inc., Palo Alto, CA 94303, USA, Abstract, pp. 8.	
		SUN MICROSYSTEMS, Sun MAJC 5200, "A Processor for the 21st Century", pp. 34.	
		TOWNER, DANIEL WILLIAM (BSc), "The 'Uniform Heterogeneous Multi-threaded' Processor Architecture", A dissertation submitted to the University of Bristol in accordance with the requirements for the degree of Doctor of Philosophy in the Faculty of Engineering, Department of Computer Science., June 2002, pp. 199.	
		TREMBLAY, MARC, "The MAJC Architecture: A Synthesis Of Parallelism And Scalability", Sun Microsystems, 901 San Antonio Rd., MS USUN03-202, Palo Alto, CA 94303, USA, marc.tremblay@sun.com, November - December 2000, pp. 25	

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wtr) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450